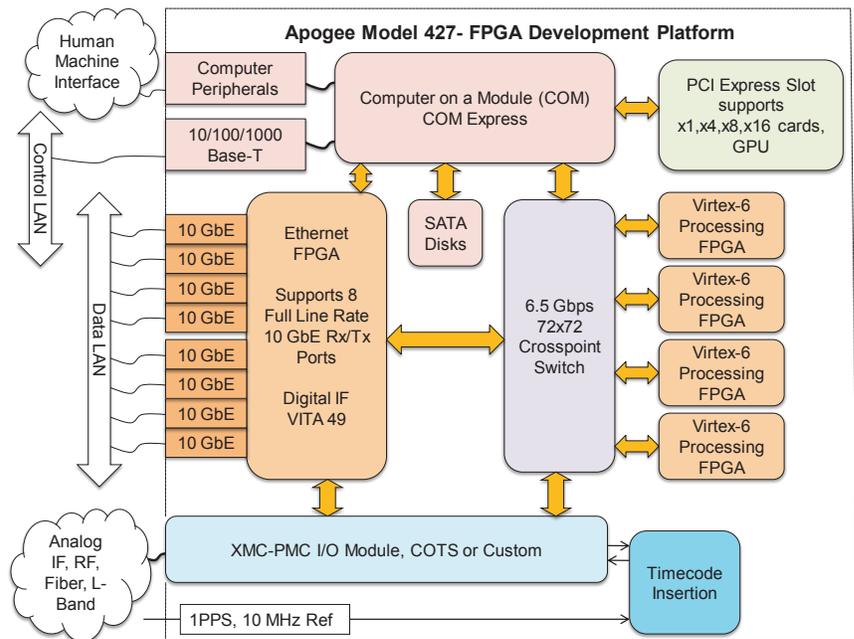


- **10 GbE Network Digital IF (SDDS or VITA 49A) Inputs and Outputs**
- **COTS XMC, PCI Express, GPU landing sites for Analog ADCs, DACs, or other types of signal Inputs/Outputs/Processing**
- **Apogee provided 10 GbE Ethernet Processor, Four User FPGAs**
- **Application development is supported with a software command plug-in and firmware module design methodology**
- **Apogee's library is made available for fast implementation of processing algorithms**
- **SDR Packet processor for formatting and un-formatting of UDP/VITA49 packets**
- **Timecode and Sample Clock compensation processor**

Apogee's flagship product, the Model 427 Software Defined Radio is a network appliance that can be utilized for sensor acquisition, extremely wideband data processing, large volume signals processing, data formatting, bandwidth reduction, information fusion, modulation-demodulation, and digital down conversion.

Apogee provides a comprehensive Application Developers Kit that enables customers to develop and host their own signal processing applications on Apogee FPGA platforms.



Apogee FPGA Development Platforms

Form Factor, Power.....	2U Rack Mount, 93-264VAC, 43-67Hz, 10A (300W Typical)
Network (UDP, VITA 49).....	8 Ports - 10Gb/s SFP+, Single Mode or 850nm Multimode
Ethernet Processor (provided by Apogee).....	Ingest up to 500 MHz Analog BW per input 10 GbE port
UDP Multicasts.....	Up to 4096 Independent Multicast Supported
CPU / OS.....	COM Express, Atom or I7, Fedora 12 OS
Landing Sites.....	COTS XMC, PCI Express x16, GPU
User FPGAs.....	Four User FPGAs - Virtex-6, LX240, SX315, or SX475
External User Memory per FPGA.....	1-GB DDR3 SDRAM @533MHz, 72-Mbits QDR SRAM @350MHz
High Speed Interconnects per FPGA.....	(4x) 1-GByte/Dir Aurora to circuit switch, (1x) 500-MByte/Dir Aurora to circuit switch
Built-In Self Test.....	Puts the unit in a looping test that verifies Memories, High-Speed Interconnects, USB interfaces, and optionally, SFPs and fibers in loopback. Reports errors to a log file .

Apogee Model 427

